

# GPIB-PCI-E

## ► PCI-Express IEEE-488.2 GPIB Interface

### Features

- PCI Express Base 1.0a compliant
- 1KByte transfer FIFO for optimum performance
- GPIB-32.DLL compatible, runs VEE, LabVIEW etc.
- Windows 2000/XP/Vista, Linux

### Overview

The GPIB-PCI-E is a PCI Express GPIB controller card that converts any PC with a PCI Express slot into a GPIB controller.

It performs all the basic IEEE-488.1 functions such as talker, listener and system controller. The IEEE-488.2 compatible functions make it fully compliant with the IEEE-488.2 specification. In controller applications, you can control typically up to 15 devices (instruments). If operated as a talker/listener (device) interface it does exchange data and state information with the current controller-in-charge of the GPIB bus. The GPIB-PCI-E lets Windows and Linux programs control GPIB devices.

### Hardware

The GPIB-PCI-E card plugs into any PCI Express x1 slot. A 24 pin STD IEEE488 connector is used to connect GPIB equipment using standard GPIB cables.

### Software

**Windows** The Windows software set is included with the GPIB-PCI-E. It is a WDM driver and supports Windows 2000, XP and Windows Vista on all PC compatible platforms. Libraries and header files are included for the Visual C++, Visual Basic, MINGW and Delphi development systems. An industry standard compatible GPIB-32.DLL supports nearly all applications designed for that interface, including applications developed for LabView 6+, LabWindows, Agilent VEE, TransEra HT-Basic, Agilent Intuilink, and more.



**Linux** The Linux software set is included with the GPIB-PCI-E. It supports the Intel (x86) platform Linux kernel versions 2.4 and 2.6. Thus it is compatible with all Linux distributions based on that kernels, e.g. RedHat, SuSE etc. Application development using the GNU Compiler Collection (GCC Version 3) is supported. The ig++ class library provides all interfaces required to control instruments. In addition, IEEE488.2/SCPI compatible instruments can be implemented using Linux based embedded systems.

## Specifications

### GPIB Capabilities

**IEEE 488.1 Capabilities:** AH1, SH1, T/TE5, L/LE3, SR1, RL1, PP1/PP2, DC1, DT1, C1, C2, C3, C4, C5

**IEEE 488.2 Capabilities:** includes the capability to read the following bus lines:EOI, ATN, SRQ, REN, IFC, NRFD, NDAC, DAV

**GPIB Handshake Rate:** > 1Mbytes/sec

### Environmental and Physical

**Size** (excluding bracket): PCI form factor, 85 mm H x 120 mm W (3.35 in x 4.73 in)

**Weight** (net): 75 g

**Operating ambient temperature:** 0 to 50°C

**Storage temperature:** -20 to 80°C

**Relative humidity:** 5 to 95%, noncondensing

## Ordering Information

GPIB-PCI-E - Card, Software CDROM

## On the Web

Click [www.inesinc.com](http://www.inesinc.com) for more information and resources.



ines Test and Measurement GmbH & Co. KG  
31542 Bad Nenndorf · DE (Germany)  
Phone +49 5723 916 250  
Fax +49 5723 916 252  
Web [www.inesinc.com](http://www.inesinc.com)

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